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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/537,517	06/03/2005	Markus Feuser	DE02 0290 US	3562
65913	7590	11/05/2008	EXAMINER	
NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131			SU, SARAH	
			ART UNIT	PAPER NUMBER
			2431	
			NOTIFICATION DATE	DELIVERY MODE
			11/05/2008	ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

Office Action Summary	Application No. 10/537,517	Applicant(s) FEUSER ET AL.	
	Examiner Sarah Su	Art Unit 2431	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 08 October 2008.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-4 and 6-12 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4 and 6-12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 October 2008 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

FINAL ACTION

1. Amendment B, received on 8 October 2008, has been entered into record. In this amendment, claims 1-4 and 6-10 have been amended, claim 5 has been cancelled, and claims 11-12 have been added.
2. Claims 1-4, 6-12 are presented for examination.

Response to Arguments

3. Applicant's arguments filed 8 October 2008 have been fully considered but they are not persuasive.

As to claims 1 and 10, it is argued by the applicant that Candelore does not disclose "the device configured such that the parameter of at least one sub-area is encrypted only in certain areas depending on at least on further sub-area." The examiner respectfully disagrees. Candelore discloses that one key is used with the high order address lines (i.e. encrypted only in certain areas) where the key depends on the address and unit key (i.e. sub-area) (col. 24, lines 6-11, 15-17).

As to claim 6, it is argued by the applicant that Candelore does not disclose "a method of securing access to an electronic memory, comprising encrypting at least one parameter, of at least one of a plurality of access-secured sub-areas of the electronic memory, wherein the parameter to be encrypted of the sub-area is encrypted only in certain areas to increase security of the electronic memory." The examiner respectfully disagrees. Candelore discloses an apparatus for efficiently and securely transferring

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blocks of program information between a secure circuit and an external storage device (i.e. electronic memory) (col. 1, lines 5-8). Candelore also discloses that one key is used with the high order address lines (i.e. encrypted only in certain areas) where the key depends on the address and unit key (i.e. sub-area) (col. 24, lines 6-11, 15-17).

Specification

4. The disclosure is objected to because of the following informalities:
 - a. in page 3, line 7: "encryption block" should read –encryption block–;
 - b. in page 3, line 9: "an encrypted address" should read –an encrypted address–.

Appropriate correction is required.

Drawings

5. The drawings were received on 8 October 2008. These drawings are acceptable.

Claim Rejections - 35 USC § 102

6. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

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7. Claims 1-7, 9-10 are rejected under 35 U.S.C. 102(b) as being anticipated by Candelore et al. (EP 0908810 A2 and Candelore hereinafter).

As to claims 1 and 6, Candelore discloses a system and method for block chaining and block re-ordering, the system and method having:

an electronic memory component (col. 19, line 58; col. 20, lines 1-3);
a plurality of access-secured sub-areas (i.e. block), **each having at least one assigned parameter** (i.e. address line), **the device configured such that the parameter of at least one sub-area is encrypted** (i.e. hashed, keyed) **only in certain areas depending on at least one further sub-area** (col. 24, lines 6-11, 15-17).

As to claims 2 and 7, Candelore discloses:

wherein the encrypted parameter is encrypted as a function of at least one parameter of the further sub-area (col. 24, lines 12-15; col. 25, lines 12-18).

As to claim 3, Candelore discloses:

wherein at least one of the input value to the function or the return value from the function is more than one bit wide (col. 21, lines 39-41).

As to claim 4, Candelore discloses:

wherein the memory component comprises an erasable programmable read only memory, an electrically erasable programmable read only memory or a flash memory (col. 19, line 58; col. 20, lines 1-3).

As to claim 9, Candelore discloses:

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wherein the access-secured sub-areas are secured separately (col. 25, lines 12-18).

As to claim 10, Candelore discloses:

an electronic memory component (col. 19, line 58; col. 20, lines 1-3) **comprising a plurality of access-secured memory areas** (i.e. block), **each having at least one assigned parameter** (i.e. address line), **the device configured such that the parameter of at least one sub-area is encrypted** (i.e. hashed, keyed) **only in certain areas depending on at least one further sub-area** (col. 24, lines 6-11, 15-17), **the data processing device utilized in at least one of a smart card controller, a reader integrated circuit, or a cryptography chipset, for application in at least one of audio or video encryption** (col. 22, lines 32-38).

As to claims 11 and 12, Candelore discloses:

wherein the at least one assigned parameter comprises an address (col. 24, lines 15-17).

Claim Rejections - 35 USC § 103

8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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9. Claim 8 is rejected under 35 U.S.C. 103(a) as being unpatentable over Candelore as in claim 7 and in view of Toh et al. (US 2002/0048372 A1 and Toh hereinafter).

As to claim 8, Candelore does not disclose:

characterized in that the function is one-to-one.

Nonetheless, this feature is well known in the art and would have been an obvious modification of the teachings disclosed by Candelore, as evidenced by Toh.

Toh discloses a system and method for generating and utilizing a universal signature object for digital data, the system and method having:

characterized in that the function is one-to-one (0012, lines 16-18).

Given the teaching of Toh, a person having ordinary skill in the art at the time of the invention would have readily recognized the desirability and advantages of modifying the teachings of Candelore with the teachings of Toh by using a one-to-one function.

Toh recites motivation by disclosing that using a one-to-one hash function allows for each hash number to generate one data file so that changes in the data can be detected (0012, lines 17-20). It is obvious that the teachings of Toh would have improved the teachings of Candelore by using a one-to-one function for encryption in order to be able to detect changes in the data.

Conclusion

10. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sarah Su whose telephone number is (571) 270-3835. The examiner can normally be reached on Monday through Friday 7:30AM-5:00PM EST..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh can be reached on (571) 272-3795. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Sarah Su/
Examiner, Art Unit 2431

/Christopher A. Revak/
Primary Examiner, Art Unit 2431